AMENDMENTS TO THE CLAIMS

Please replace all previous versions of the claims with the following listing:

1. (Cancelled)

2. (Cancelled)

3. (Cancelled)

4. (Cancelled)

5. (Cancelled)

6. (Cancelled)

7. (Currently Amended) A method of avoiding improper machine activation by machine control parameters of a multi-axis machine tool, comprising:

assigning a private encryption key and a private decryption key to a sender of the machine control parameters using a processor of a computer system, wherein the private encryption key is different from the private decryption key and is provided for the decoding;

first encoding the machine control parameters intended for the multi-axis machine tool using the processor ~~computer system~~ and the private decryption key;

providing the first encoded machine control parameters with a sender identification of a sender using the processor ~~computer system~~;

second encoding the ~~provided~~ first encoded machine control parameters using the processor ~~computer system~~ and an encryption key that is assigned to the multi-axis machine tool;

first decoding the second encoded machine control parameters using the processor ~~computer system~~ and a decryption key that is assigned to the multi-

axis machine tool, wherein the decryption key is different from the encryption key and is provided for the decoding;

authenticating a sender ~~by the sender's~~ based on a sender identification and a suitability of the ~~private~~ encryption key assigned to the sender for the first decoded machine control parameters using the processor; ~~computer system; and, if so,~~

if a sender is authenticated, second decoding the first decoded machine control parameters using the processor ~~computer system~~ and the ~~private~~ encryption key;

checking whether the machine control parameters were actually generated for said multi-axis machine tool using the processor; and

determining whether a module associated with a sender which generated the machine control parameters is actually suitable and authorized to do so using the processor.

8. (Cancelled)

9. (Cancelled)

10. (Previously Presented) A computer system comprising at least one data processing unit and at least one memory, wherein the data processing unit is set up in programming terms in such a way that it works on the basis of the method according to Claim 7.

11. (Cancelled)

12. (Cancelled)

13. (Cancelled)

14. (Cancelled)

15. (Cancelled)

16. (Cancelled)

17. (Cancelled)

18. (Cancelled)

19. (Cancelled)

20. (Cancelled)

21. (Cancelled)

22. (Cancelled)

23. (Cancelled)

24. (Cancelled)

25. (Cancelled)

26. (Cancelled)

27. (Cancelled)